

FIG. 3



SPECIFICATION	IMPLICATION											
(1) V1 = vector{a(0:9)}	a[0] ‱	% .	a[2] b[1]		Z	W .		%	<i>70</i> .	a[9]		
(2) V2 = vector (stride 2) {b(0:4)}	Ç		Ç		Ç		b[3]		Ç			
(3) $V3 = vector\{c(0,5,6:8) d(0:3:2)\}$	c[0]	※ c[5]	ඎ c[6]		88 c[8]		∰ d[2]					
(4) V4 = vector{skip(2) j(0:1)}												
(5) V5 = step{interleave{v3 v4} i[(0)]}	& c[0]		c[5]	ניזן	5 5 6		燚 c[7]			∰ d[0]	∰ d[2]	∭ i[0
(6) V6 = merge{V1 V2}	Q Ø	%	Ç; ∭ Ma	cro			Q W	%	Ç:	%		
(7) V7 = interleave {vector{Tall(0:4)} merge{vector{x(0:4)} vector{y(0:4)}		₩ Wacr						oth	er			•
(8) hg = Vabut{V6 step{v7 zzinst}}	Q.		Q	nen XXX				33335 33335 3335 3335 3335 3335 3335 3	Q		nst	
	0	1	2		Bits		6 > ,					

FIG. 4



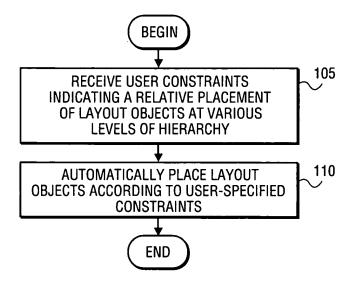


FIG. 1

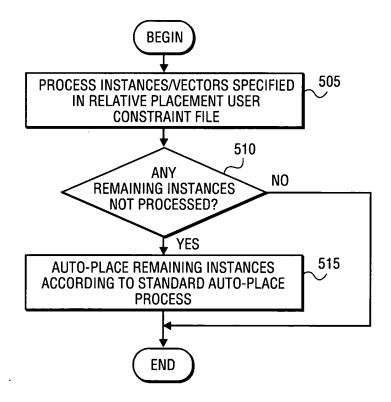


FIG. 5



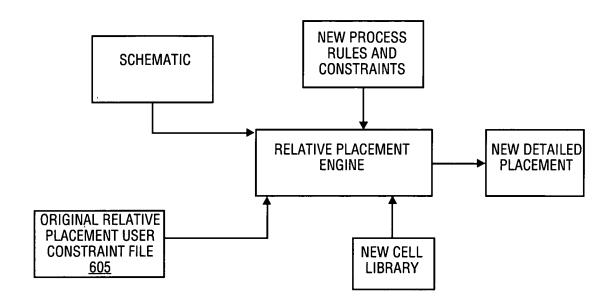


FIG. 6



